From: noreply@wsu.edu
To: curriculum.submit

Subject: 667541 Electrical Engineering and Computer Science Requirements Revise - Revise or Drop Graduate Plan

 Date:
 Friday, September 10, 2021 2:32:19 PM

 Attachments:
 2021.09.10.14.28.52.87.FormData.html

2021.09.10.14.28.50.40.currentCatalogFile CS PhD.pdf

2021.09.10.14.28.50.40.currentCatalogFile1 Final Draft CS PhD 8.18.2021.pdf

Jessica Cross has submitted a request for a major curricular change. His/her email address is: j.cross@wsu.edu.

Requested change: Revise or Drop Graduate Plan

Degree: PhD Computer Science

Title: Ph.D. Computer Science

Requested Effective Date: Fall 2022

Revise plan requirement: Yes

Dean: Field, David - Assoc Dean - VCEA - Grad,

Chair: Pande, Partha – Director – Electrical Engineering and Computer Science,

Catalog Subcommittee AAC, PHSC, or GSC Faculty Senate Approval Date Approval Date Approval Date From: Pande, Partha Pratim
To: curriculum.submit
Cc: Field, Dave

Subject: Re: 667541 Electrical Engineering and Computer Science Requirements Revise - Revise or Drop Graduate Plan

Date: Friday, September 10, 2021 3:17:10 PM

I approve this proposal in its current form

Partha Pratim Pande, FIEEE
Director and Professor
Boeing Centennial Chair in Computer Engineering
School of EECS, WSU
PO BOX 642752
Pullman, Washington 99164-2752
Phone 509-335-5055 Fax 509-335-3818
pande@wsu.edu

From: curriculum.submit@wsu.edu <curriculum.submit@wsu.edu>

Date: Friday, September 10, 2021 at 2:28 PM **To:** Pande, Partha Pratim <pande@wsu.edu>

Cc: Field, Dave <dfield@wsu.edu>

Subject: 667541 Electrical Engineering and Computer Science Requirements Revise - Revise or

Drop Graduate Plan

Pande, Partha – Director – Electrical Engineering and Computer Science,

Field, David - Assoc Dean - VCEA - Grad,

Jessica Cross has submitted a request for a major curricular change.

Requested change: Revise or Drop Graduate Plan

Degree: PhD Computer Science

Title: Ph.D. Computer Science

Requested Effective Date: Fall 2022

Revise plan requirement: Yes

Both Chair and Dean approval is required to complete the submission process. Please indicate that you have reviewed the proposal by highlighting one of the statements below and **reply all** to this email. (<u>curriculum.submit@wsu.edu</u>.) [Details of major change requested can be found in the attached supplemental documentation]

From: Field, Dave

To: Pande, Partha Pratim; curriculum.submit

Subject: Re: 667541 Electrical Engineering and Computer Science Requirements Revise - Revise or Drop Graduate Plan

Date: Friday, September 10, 2021 4:20:50 PM

I approve this proposal in its current form

David Field

Professor, School of Mechanical and Materials Engineering
Associate Dean for Research, Voiland College of Engineering and Architecture
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510 Carpenter Hall
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Pullman, WA 99164-2250

ph. 509-335-3524

https://labs.wsu.edu/dfield/

https://www.journals.elsevier.com/materials-characterization

https://icdream.org/

From: Pande, Partha Pratim <pande@wsu.edu> Sent: Friday, September 10, 2021 3:17 PM

To: curriculum.submit <curriculum.submit@wsu.edu>

Cc: Field, Dave <dfield@wsu.edu>

Subject: Re: 667541 Electrical Engineering and Computer Science Requirements Revise - Revise or

Drop Graduate Plan

I approve this proposal in its current form

Partha Pratim Pande, FIEEE
Director and Professor
Boeing Centennial Chair in Computer Engineering
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We are requesting to update the total graded credits required for the current Computer Science Ph.D. program as well as the content of those required classes. The reduced overall number of credits allows students to focus more extensively on their research while still allowing them to take relevant classes prior to their qualifier and preliminary exams. This change will also put us in the range of required courses currently used by comparable programs in the U.S.

The change to the content of the required courses reflects updates to the course offerings that are available on the Pullman and Tri-Cities campuses which will better serve the availability of these courses to all students in the program.

Electrical and Computer Engineering PhD:

- ◆ Core: 3 course minimum:*
 - - *Note: A grade of B or higher is required on all courses used for this requirement.
 - **Note: Only one course from E E 503 and E E 555 may count in this requirement.
- Minor Area Systems:
 - Select 2 courses in consultation with your committee and pass each with a grade of B+ or higher.
- Minor Area Power:
 - Select 2 courses in consultation with your committee and pass each with a grade of B+ or higher.
- Minor Area Microelectronics:
 - Select 2 courses in consultation with your committee and pass each with a grade of B+ or higher.
- Minor Area Electrophysics:
 - Select 2 courses in consultation with your committee and pass each with a grade of B+ or higher.
- Minor Area Computer Engineering:
 - Select 2 courses in consultation with your committee and pass each with a grade of B+ or higher.
- Focus Areas:
 - o Computer Engineering
 - Core Courses
 - Take at least two of the following courses; must be completed w/ B or better
 - E E 524/CPT S 561 Advanced Computer Architecture
 - E E 586 VLSI Systems Design
 - NOTE: If E E 586 is not available, E E 466 (VLSI Design)
 may be substituted
 - o E E 587 System on Chip Design and Test
 - NOTE: If E E 587 is not available, E E 434 (ASIC and Digital Systems Design) may be substituted
 - Take at least one additional course from the following list; must be completed w/ B or better
 - o <u>E_E 501 Linear System Theory</u>
 - <u>E_E 503 Structure, Dynamics, & Control of Large-Scale</u>
 Networks
 - o E E 507 Random Processes in Engineering
 - E_E 518 Advanced Electromagnetic Theory I
 - E E 521 Analysis of Power Systems
 - o <u>E E 523 Power Systems Stability & Control</u>
 - o E E 524/CPT S 561 Advanced Computer Architecture
 - o <u>E E 555 Computer Communication Networks</u>

- o E E 571 Advanced Wireless Integrated Circuits & Systems
- E E 582 Advanced Topics: Cyber Security
- E E 586 VLSI Systems Design
- E_E 587 System on Chip Design and Test
- o <u>E E 596 Advanced Analog Integrated Circuits</u>
- CPT S 516 Algorithmics

o <u>Electrophysics</u>

- Core Courses
 - Take at least two of the following courses; must be completed w/ B or better
 - E E 518 Advanced Electromagnetic Theory I
 - o E E 535 Numerical Solutions to EM Problems
 - <u>E E 571 Advanced Wireless Integrated Circuits & Systems</u>
 - Take at least two additional courses from the following list; must be completed w/ B or better
 - o <u>E E 501 Linear System Theory</u>
 - <u>E E 503 Structure, Dynamics, & Control of Large-Scale</u>
 Networks
 - o E E 507 Random Processes in Engineering
 - <u>E E 518 Advanced Electromagnetic Theory I</u>
 - <u>E_E 521 Analysis of Power Systems</u>
 - E E 523 Power Systems Stability & Control
 - o <u>E E 524/CPT S 561 Advanced Computer Architecture</u>
 - o E E 535 Numerical Solutions to EM Problems
 - <u>E_E 555 Computer Communication Networks</u>
 - o <u>E E 571 Advanced Wireless Integrated Circuits & Systems</u>
 - o <u>E E 582 Advanced Topics: Cyber Security</u>
 - E E 586 VLSI Systems Design
 - <u>E E 587 System on Chip Design and Test</u>
 - o <u>E E 596 Advanced Analog Integrated Circuits</u>
 - Any further courses as determined by your committee chair/faculty

Microelectronics

- Core Courses
 - The following courses must be taken at WSU (cannot be transferred in) and must be completed with a B or better
 - <u>E E 571 Advanced Wireless Integrated Circuits & Systems</u>
 - o E E 596 Advanced Analog Integrated Circuits
- o Power
 - Core Courses
 - The following courses must be taken at WSU (cannot be transferred in) and must be completed with a B or better
 - E E 521 Analysis of Power Systems
 - <u>E E 523 Power Systems Stability & Control OR E E 582 Cyber-Power Systems</u>
 - One additional 500-level course in the Power area

- One additional core course from another focus area (not in Power)
- o <u>Systems</u>
 - Core Courses
 - The following courses must be taken at WSU (cannot be transferred in) and must be completed with a B or better
 - o Systems Core
 - E E 501 Linear System Theory
 - <u>E E 503 Structure, Dynamics, & Control of Large-Scale</u>
 Networks
 - <u>E E 507 Random Processes in Engineering</u>
 - Minor Areas two 500-level courses in a breadth area, must be completed with a B or better:
 - Power
 - <u>E_E 521</u>
 - <u>E E 523</u>
 - Microelectronics
 - E E 571
 - E E 576
 - Electrophysics
 - <u>E E 518</u>
 - E E 535
 - Computer Engineering (any two of the following)
 - E E 524/CPT S 561
 - <u>E E 586</u>
 - E E 587
 - Computer Science (any two of the following)
 - CPT_S 534 Neural Network Design & Application
 - CPT S 540 Artificial Intelligence
 - CPT S 570 Machine Learning
 - CPT S 577 Structured Prediction
 - CPT S 415 Big Data
 - <u>CPT_S 571 Computation Geno</u>mics
 - CPT S 575 Data Science
 - CPT S 580 Special topics on Data Science related topics
 - CPT S 591 Elements of Network Science
 - CPT S 411 Introduction to Parallel Computing
 - CPT S 483 Special topics course (Intro to Compilers & LLVM)
 - CPT S 527 Computer Security
 - CPT S 542 Computer Graphics
 - <u>CPT S 561 Computer Architecture</u>
 - CPT S 566 Embedded Systems
 - CPT S 580 Advanced GPU Programming and other topics related to topics in computer systems

- CPT S 484 Software Requirements
- CPT S 543 Human-Computer Interaction
- CPT S 580 Special topics on Software Engineering topics
- CPT S 581 Software Maintenance
- CPT S 582 Software Testing
- CPT S 583 Software Quality
- CPT_S 587 Software Design & Architecture
- One additional 500-level course from the following list or the previous breadth areas, must be completed with B or better. This course cannot be used to fulfill a previously listed requirement.
 - o <u>E E 501 Linear System Theory</u>
 - <u>E E 503 Structure, Dynamics, & Control of Large-Scale</u>
 Networks
 - o <u>E E 507 Random Processes in Engineering</u>
 - E E 518 Advanced Electromagnetic Theory I
 - o E E 521 Analysis of Power Systems
 - o E E 523 Power Systems Stability & Control
 - o E E 524/CPT S 561 Advanced Computer Architecture
 - o E E 555 Computer Communication Networks
 - o <u>E E 571 Advanced Wireless Integrated Circuits & Systems</u>
 - o E E 582 Advanced Topics: Cyber Security
 - o <u>E E 586 VLSI Systems Design</u>
 - E_E 587 System on Chip Design and Test
 - o E E 596 Advanced Analog Integrated Circuits
 - o CPT S 516 Algorithmics
- One additional 500-level Systems course, must be completed with B or better.
- Two additional 3 credit courses, which may be any combination of 5xx, 4xx, or 595, must be completed with B or better.
- Research Credits: 30 42 credits minimum:
 - o E E 800
- Total Graded Credits: <u>34-24</u> credits minimum
- Total Credits: 72 credits minimum

Applicable Graduate School Requirements:

Graded Credits: 34 credits Research Credits: 20 credits

o E_E 800

• Total Credits: 72 minimum

^{*}Note: A grade of B grade or higher is required on all graded courses

Hi Praveen,

They are meant to just be minimums for the degree requirements, so students could do more graded courses or more research credits as desired to reach the Grad School requirement of 72. Please let me know if this is something we will need to update.

Thank you!

On behalf of the catalog subcommittee, i am reviewing the request for updating the total graded credits for PhD CS and EE programs.

I see you are increasing the research credits from 30 to 42 minimum towards thesis and decreasing graded credits from 34 to 24 minimum (approx 8 courses).

But, the total of 42+24 = 66 minimum does not add upto 72. Am i missing something?

Regards,

Praveen