Effects of Triboelectrostatic Charging Between Polymer Surfaces in Manufacturing and Test of Integrated Circuit Packages

Rahul Panat, Jinlin Wang, and Edward Parks

Abstract—Integrated circuit (IC) package assembly and test processes include several industrial steps involving physical contact and/or friction between polymer surfaces of handlers and packages. The trend toward smaller and sleeker gadgets has resulted in a dramatic reduction in the package weight due to thickness and size miniaturization. In this paper, we show that the forces created through triboelectrostatic charging between surfaces of handlers and packages are comparable with the weight of thin and small IC packages. Furthermore, the voltages generated in such triboelectric interactions are rather small—typically below the concern levels set in the IC industry. The results show that for IC packages with a subgram weight, the impact of the triboelectrostatic force on the assembly processes is significant and occurs at low static voltages (<200 V). The data presented in this paper provides valuable insights into the selection of materials for handler designs in contact with IC packages during assembly and test processes.

Index Terms—Assembly, electrostatic charging, friction, integrated circuit, triboelectricity,

I. INTRODUCTION

RIBOELECTRIC/TRIBOELCTROSTATIC charging involves a transfer of electrons or ions between two physically contacting surfaces. This phenomenon has found uses in many applications including waste management [1], polymer separation [2], and coal separation [3]. Although useful in some industries, the static electricity (caused by friction, i.e., tribological reasons, or by other means) can cause problems in industries where contacting moving parts are commonly found; for example, semiconductor industry, piping industry [4], and so on. It is well known that a static charge developed in equipment close to a chip can cause electrostatic discharge (ESD) that can destroy the silicon circuitry [5]–[7].

The triboelectric charging involves one contacting material losing electrons and the other contacted material gaining them. The ability of materials to realize the triboelectric charging

Manuscript received October 3, 2013; revised December 30, 2013; accepted January 26, 2014. Date of publication February 20, 2014; date of current version May 1, 2014. Recommended for publication by Associate Editor W. D. Brown upon evaluation of reviewers' comments. (Corresponding author: R. Panat.)

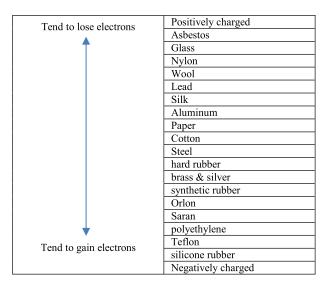
The authors are with the Assembly and Test Technology Development, Intel Corporation, Chandler, AZ 85226 USA (e-mail: rahul.panat@intel.com; jinlin.wang@intel.com; edward.a.parks@intel.com).

Color versions of one or more of the figures in this paper are available online at http://ieeexplore.ieee.org.

Digital Object Identifier 10.1109/TCPMT.2014.2303985

¹The two terms are synonymous in the literature and we have used them interchangeably in this paper.

TABLE I
TRIBOELECTRIC SERIES [8], [9]



depends upon the relative tendencies of the materials to have electron transfer, which can be graded in a triboelectric series shown in Table I [8], [9]. The materials on the top of the Table I have a stronger tendency to lose electrons compared with those at the bottom. The triboelectric series are qualitative since the material rank also depends upon the test conditions, such as shearing speed, rubbing pressure, contact time, and humidity. Furthermore, the charge on the conductor surface is uniform while it varies by location in the case of an insulator [8]. Polymeric materials used in handlers in integrated circuit (IC) assembly and test are typically negatively charged upon friction. The interaction of functional groups in the polymers with their triboelectrical properties was examined in [10]. They constructed a tribocharging series that includes a wide range of synthetic and natural polymers. Their results revealed that the charging develops from the transfer of protons between the contacting surfaces. Soh et al. [11] and Cademartiri et al. [12] concluded that the charge transfer can occur via ions rather than by electrons in some cases. In addition to physical contact, charges can build up when a high electrostatic field develops between two closely located objects [5]–[7], [13].

Although efforts have been made to resolve the ESD issue for handling of silicon [13], limited published information exists for the triboelectrostatic charging forces created in

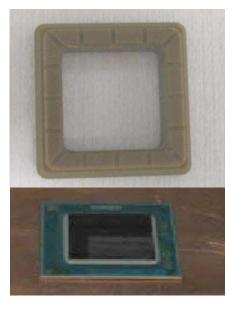


Fig. 1. Image of the handler (top) and an integrated circuit package (bottom) used in the experiments.

the handlers used in the IC assembly and test processes. These forces are expected to be encountered in the pick-and-place processes, where the die or package is usually picked up (by a vacuum or otherwise) and then placed (for example, by turning off the vacuum). In the past, these Coulomb forces resulting from triboelectrostatic charges were countered by the weight of the IC packages preventing any sticking of the surfaces.

In this paper, we investigate the issue of sticking of polymer surfaces in IC assembly and test due to forces generated by triboelectrocharging. The surfaces chosen are handler and IC package (polymer–polymer) and Si and handler (ceramic–polymer), as observed typically in industry. The triboelectrostatic forces between these surfaces are shown to be comparable with the weight of the packages indicating their increasing importance in the handling of thinner, smaller, and lighter ICs. Furthermore, the voltage measured across these surfaces is shown to be low (less than 200 V), typically below the alarm levels set in the semiconductor industry for ESD. These finding are highly useful to industry in providing guidelines to prevent process malfunction and/or package surface property modifications under high-volume manufacturing environment.

II. EXPERIMENTAL SETUP

The experimental setup was designed to mimic the industrial processes and materials. The polymer surface to mimic handlers in industry was chosen to be polyamide-imide, industrially known as torlon [14]. Torlon is a high-strength and high wear-resistant thermoplastic with a rank in the triboelectric table close to that of teflon [8], [9], indicating its tendency to easily gain electrons with friction. A typical IC package surface (package in high-volume manufacturing) with photodefinable thermoset polymer from the acrylate family was chosen to represent the polymer contacting the handler. Pieces of silicon wafer, Cu, and Al were also used as other contacting

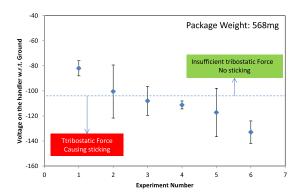


Fig. 2. Voltage versus sticking of the handler with an integrated circuit package.

surfaces to further gain an insight into this problem. The voltages and forces between the torlon handler and the package were measured by a commercially available voltmeter [15]. The humidity and temperature near the experimental setup were carefully recorded. A torlon pusher along with the Intel package is shown in Fig. 1.

To measure the electrostatic forces between the pusher and the package, a piece of silk cloth was rubbed against the pusher to generate a static charge. The pusher was then taken in contact with the package as well as the pieces of silicon, aluminum, and copper of known weight on an insulating table. As expected from the triboelectric series, the voltmeter showed the cloth positively charged and the pusher negatively charged after rubbing. The distance between the voltmeter (i.e., field meter) and the pusher was kept at 25 mm. This distance was specified by the manufacturer of the field meter to get accurately calibrated results. Note that the pusher voltage (upon rubbing against the piece of cloth) was observed to vary from location to location. Accordingly, the voltage was measured on four sides of the pusher for each of the tests. To rule out the possibility of contaminant-based sticking between the surfaces, a detailed surface chemical analysis (not discussed in this paper) of the handler and the integrated circuit packages was performed to confirm that there were no organic contaminants.

III. RESULTS AND DISCUSSION

The torlon voltage after grounding was measured (consistently ~ 25 V) and subtracted from the torlon voltages reported post rubbing of the silk cloth. Fig. 2 shows the voltage of the handler as a function of the sticking phenomenon. It was observed that the sticking occurred above a voltage of about 110 V. Each data point of Fig. 2 shows the average of the voltage of the four corners of the pusher shown in Fig. 1. The error bar is equal to the standard deviation of the four data points per unit.

Considering that the handler area was about 125 mm², we conclude that at 110 V, the force per unit area between the handler and the package is about 4.56 Pa (0.57 g/125 mm²). Thus, the force per unit area per unit charge is 0.0415 Pa/V. Once the sticking occurred, we observed that the IC package could easily be moved around the pusher as long as no

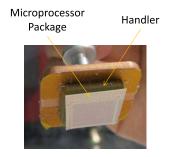


Fig. 3. Integrated circuit package sticking with the handler due to trioelectrostatic forces.

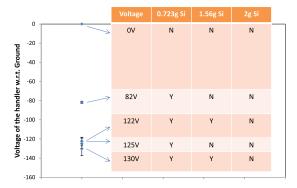


Fig. 4. Map showing sticking of flat Si pieces with the handler for different weights of Si. Y indicates that the triboelectrostatic force is sufficiently high for Si to stick with the handler and N indicates otherwise.

conductor touched the contact area of the handler with the IC package. Fig. 3 shows the phenomenon of package sticking with the handler due to the triboelectrostatic forces.

Similar to the IC package, Si and aluminum (flat) pieces also showed thresholds below 200 V for submilligram weight. Fig. 4 shows the map of sticking of Si pieces to the handler as a function of the static voltage.

It is clear that in case of Si, the sub-1-g piece is the most vulnerable for triboelectric charging with the vulnerability reducing as the weight increases. The small discrepancy we observed at 122 V (force appears to be higher for 122 versus the 125 V) is attributed to the variability in measurement observed in an experimental environment. Note that we repeated above experiment with a rough and polished Si wafer and found no difference in the results. We thus discount the possibility of sticking of the Si to handler due to the effect of smooth contact causing a suction effect. In case of Si, the force at \sim 82 V is >723 mg, while that for a IC package at 110 V is ~570 mg under identical moisture and temperature conditions. This indicates that Si is more vulnerable to the triboelectrostatic forces as compared with integrated circuit packages for the handler material selected for this paper. Obviously, a systematic study of all the materials used in semiconductor packages can map the relative risks and vulnerabilities of the triboelectrostatic forces in IC assembly

A similar plot for flat Cu pieces is shown in Fig. 5. Clearly, the 600-mg Cu can be picked up somewhere between 85 and 110 V indicating a similar behavior as that for the packages.

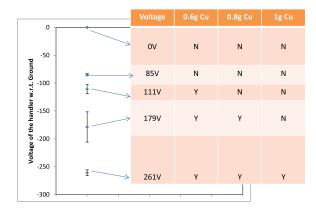


Fig. 5. Map showing sticking of flat Cu pieces with the handler for different weights of Cu. Y indicates that the triboelectrostatic force is sufficiently high for Cu to stick to the handler and N indicates otherwise.

In case of the metal sheets, a small touch from a conductor to the sheet resulted in the separation of the sheet confirming the role of static electricity in the sticking process. Note that although we had to use an external means of creating static electricity in the handler, the actual industrial processes do involve a localized relative motion between the processor and handlers.

We note that our voltage measurement has a precision of ± 1 V. The RH and temperature during these experiments were 40% and 25 °C, respectively. The actual conditions in the clean rooms used in industry may be slightly different depending upon the class of the clean room. Furthermore, although we discuss this phenomenon for packages, we also show similar triboelectrostatic behavior for Si. This indicates that this phenomenon may be relevant to Si fabs, but the weight of the wafers is expected to easily counter the triboelectrostatic forces. Finally, the charge density varies on the surface of the polymers and the distance to the contacting surface is not a constant from point to point. The data presented in this paper, however, clearly show that for thin and small packages (typically with a sub-gram weight), the impact of the triboelectrostatic force on the assembly process can be significant even if the voltages are rather low. Several solution paths need to be engineered to solve these issues. A systematic study characterizing the triboelectrostatic behavior of different contacting materials in semiconductor packaging industry is needed to develop materials and design guidelines for handlers and other contacting equipment parts.

IV. CONCLUSION

The attraction forces caused by static electricity due to frictional contact between polymer–polymer interfaces in ICs can impact the assembly and test processes. This effect is increasingly important as the processors become lightweight in response to the industry wide miniaturization of electronic devices and systems. In this paper, we demonstrate that the triboelectrostatic forces in assembly and test are comparable with the weight of thin and light ICs at voltages lower than those for which alarms are set in the semiconductor industry. Similar behavior is demonstrated in the case of contacting polymer–Si and polymer–metal surfaces. These results provide

useful guidelines to industry in designing the handlers and other process equipment in semiconductor assembly and test.

ACKNOWLEDGMENT

The authors would like to thank J. Neeb, Dr. P. Tiwari, and S. Ongchin for helpful discussions. They would also like to thank L. Mehr, P. Martin, and M. Walk for encouragement.

REFERENCES

- G. Wu, J. Li, and Z. Xu, "Triboelectrostatic separation for granular plastic waste recycling: A review," *Waste Manag.*, vol. 33, no. 3, pp. 585–597, Mar. 2013.
- [2] G. Dodbiba, A. Shibayama, T. Miyazaki, and T. Fujita, "Triboelectrostatic separation of ABS, PS and PP plastic mixture," *Mater. Trans.*, vol. 44, no. 1, pp. 161–166, 2003.
- [3] J. Baltrus, J. Diehl, Y. Soong, and W. Sands, "Triboelectrostatic separation of fly ash and charge reversal," *Fuel*, vol. 81, no. 6, pp. 757–762, Apr. 2002.
- [4] C. Dragan, M. Bilici, S. Das, and L. Dascalescu, "Triboelectrostatic phenomena in suction-type dilute-phase pneumatic transport systems," *IEEE Trans. Dielectr. Electr. Insul.*, vol. 16, no. 3, pp. 661–667, Jun. 2009.
- [5] J. Vinson and J. Liou, "Electrostatic discharge in semiconductor devices: An overview," *Proc. IEEE*, vol. 86, no. 2, pp. 399–418, Feb. 1998.
- [6] A. Khan, Electro-static Discharge (ESD) Tutorial. San Jose, CA, USA: Cypress Semiconductor Corp., Mar. 2012.
- [7] S. H. Voldman, ESD Basics: From Semiconductor Manufacturing to Product Use. New York, NY, USA: Wiley, 2012.
- [8] T. B. Jones. (2012). Triboelectric Charging of Common Objects [Online]. Available: http://www.ece.rochester.edu/~jones/ demos/triboseries.html
- [9] AlphaLab, Inc. Salt Lake City, UT, USA. (2013). Electrostatic Formulas for Force, Voltage, Discharge Time Etc. on Charged Samples or Surfaces [Online]. Available: http://www.trifield.com/content/triboelectric-series/
- [10] A. F. Diaza and R. M. Felix-Navarro, "A semi-quantitative triboelectric series for polymeric materials: The influence of chemical structure and properties," *J. Electrostat.*, vol. 62, no. 4, pp. 277–290, Nov. 2004.
- [11] S. Soh, S. W. Kwok, H. Liu, and G. M. Whitesides, "Contact de-electrification of electrostatically charged polymers," *J. Amer. Chem. Soc.*, vol. 134, no. 49, pp. 20151–20159, Nov. 2012.
- [12] R. Cademartiri, C. A. Stan, V. M. Tran, E. Wu, L. Frair, D. Vulis, et al., "A simple two-dimensional model system to study electrostatic-self-assembly," Soft Matter, vol. 8, no. 38, pp. 9771–9791, Jul. 2012.
- [13] S. H. Voldman, "Electrostatic discharge (ESD) and latchup in 3-D memory and system on chip applications," in *Proc. IEEE 11th ICSICT*, Nov. 2012, pp. 1–3.
- [14] Solvay Advanced Polymers LLC. Alpharetta, GA, USA. (2002). *Torlon Resin Engineering Data* [Online]. Available: http://www.solvayplastics.com/sites/solvayplastics/EN/specialty_polymers/Spire_Ultra_Polymers/Pages/Torlon.aspx
- [15] Prostat PFM-711A Electrostatic Field-Meter Operation Manual, Prostat Corp., Bensenville, IL, USA, 2007.



Rahul Panat received the B.S. degree in mechanical engineering from the College of Engineering, Pune, India, the M.S. degree in mechanical engineering from the University of Massachusetts, Amherst, MA, USA, and the Ph.D. degree from the Department of Theoretical and Applied Mechanics, University of Illinois at Urbana-Champaign (UIUC), Urbana, IL, USA, in 2004.

Dr. Panat worked on the thermodynamics of stressdriven diffusion in thermal barrier systems at UIUC.

He has been with Intel's Microelectronic Manufacturing R&D center at Chandler, AZ, USA since 2004. He has been involved in several problems such as the lead-free conversion of flash memory processors, fine line-space substrate development, and MLCC passives and their integration onto Intel packages. He was the lead process R&D Engineer for world's first "halogen-free" (green) IC chip by eliminating halogenated flame retardants from its constituents. He is currently involved in R&D problems related to the packaging of mobile as well as server microprocessors. Dr. Panat is also an Adjunct Research Faculty with the Arizona State University, Tempe, AZ, USA, working on Li-ion battery materials. He has authored 15 publications in journals, such as Nature Communications, Acta Materialia, and the Journal of Applied Physics. He also holds two patents.



Jinlin Wang received the Ph.D. degree in chemical engineering from Purdue University, West Lafayette, IN, USA, in 1995.

He is a Senior Materials Analyst with the Assembly Materials Characterization Laboratory, Intel Corporation, Chandler, AZ, USA, in 1998. Since then, he has been involved in material characterizations of packaging materials.



Edward Parks received the B.S.IT. degree from the University of Phoenix, Phoenix, AZ, USA, in 2006.

He has been with Intel, Chandler, since 1995, where he has been specializing in defect reduction, yield analysis, and electrical validation of microprocessors.

Mr. Parks has received several departmental and division recognition awards at Intel for his research on fungible thermal solutions for microprocessors, defect reduction on Fab processes, his training mentorship that enabled assembly factory ramps, and his

leadership on software integration into Intel's microprocessor test module.